## IN THE SPECIFICATION

*Please replace the 3<sup>rd</sup> paragraph on page 18 with the following.*

In step S4, the trace data is latched from the start frame of the trace memory 6, and the instruction address at the uncompressed frame is set as a base address. In step S6, the instruction code of the address is read from the base address, and the instruction is displayed. In step S7, the read pointer is incremented, and the next trace frame is read out. At step S8, it is determined whether the end of the frame is reached.